

## **intacs® certified Hardware SPICE® based on PAM4.0**

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### **Goal**

This training explains the Hardware SPICE specific processes in Automotive SPICE® PAM 4.0 and HW SPICE PAM 3.0 relationships and prepares participants for the intacs® certified Hardware SPICE® exam. Assessors will learn the basics of Hardware SPICE to enable sound assessments in the future using Hardware SPICE and in case of Hardware SPICE assessments at least one assessor in the team needs to have this qualification.

### **Content**

- Introduce the HWE PRM/PAM for process experts and assessors with Automotive SPICE®PRM/PAM know-how (for participants without Automotive SPICE®know-how, an additional training module is provided)
- Explanation of key concepts of the HWE PRM/PAM.
- Explanation of the four HWE processes and their rationales.
- Train assessors to understand HWE PRM/PAM
- Expectation of the HWE PRM/PAM in practical use.
- Some of the critical differences between hardware and software development cycles.
- Prepare assessors to perform assessments, including the extension to hardware engineering
- Prepare assessors to organize all phases of assessments (planning, performing, reporting).
- Extend Automotive SPICE®assessments to the processes defined in the HWE PRM/PAM.
- Ensure the consistent application of the Plug-in concept (including the incorporated rating guidelines).

### **Target Group and Prerequisites**

All intacs®certified Competent or Principal Assessors for Automotive SPICE®who want to lead assessments according to the HWE PRM/PAM must attend the additional training course. The precondition for attending this training is being at least an intacs®certified Process Expert or Provisional Assessor.

### **Duration**

2 Days

## Schedule

from	to	Day 1	Day 2
9:00 am	12:30 noon	Introduction  Key Concepts  HWE.1 Hardware Requirements Analysis  HWE.2 Hardware Design	HWE Work Products  SYS Processes – interpretation  Non ENG Processes – interpretation
12:30 noon	1:30 pm	<b>Lunch</b>	
1:30 pm	5:30 pm	Exercise  HWE.3 Verification against Hardware Design  HWE.4 Verification against Hardware Requirements  Semiconductor Perspective	Conclusion  Exam

## Training Materials

The course materials are available in English. Each participant will receive the training materials as well as a certificate of attendance.

## Examination and Certification

Exams are organised by VDA and performed in the afternoon of the last training day. The exams are available in German or English.

45 mins. exam time (60 min. if the exam language is not your native language)

The exam is single choice (true/false) and open book.

The exam contains 30 questions.

Each question can be answered with True or False.

Each correct answer gives one point.

You need to gain more than 75% of the points to pass (i.e., 22 correct out of 30 questions)

## **Cancellation**

Cancellation is not possible. You may determine a substitute or attend the course at a later date.

## **The EuroSPI Academy**

The training is held in the EuroSPI academy in cooperation with ISCN. The company ISCN is a certified training partner of VDA-QMC and Intacs® for Automotive SPICE (<https://www.iscn.com/ressources/PDFs/ISO330xx-intacs-cert-iscn.pdf>, <https://www.intacs.info/training-center>).

The EuroSPI Academy (<https://academy.eurospi.net>) was founded in 2021 in cooperation with the ASA (Automotive Skills Alliance) and offers an advanced online training environment with materials, templates and exercises. EuroSPI and ISCN are full partners of the ASA (<https://automotive-skills-alliance.eu/#partners>).